

**EXPEDITED PROCEDURE UNDER 37 CFR § 1.116**  
**GROUP ART UNIT 2153; EXAMINER *M. Winters***  
**PATENT**  
**IBM Docket No. POU920000126US1**      **09/619,051**

**Listing of Claims**

1. (Original) A method for sending a message stored in the memory of a first data processing system into the memory of a second data processing system, said method comprising the steps of:

transmitting said message from said first data processing system to a temporary memory in an adapter which is connected to said second data processing system;

transferring, from said adapter to said second data processing system, an indication that said temporary memory in said adapter contains the message received from said first data processing system;

transferring, from said second data processing system to said adapter, real address information indicating desired target memory locations for said message;

transferring said message, from said temporary memory in said adapter, directly into said target memory locations in the memory of said second data processing system, said transfer occurring via direct memory access;

transferring, from said adapter to said second data processing system, an indication that said target locations now contain the message received from said first data processing system; and

transmitting an acknowledgment of receipt of said message from said second data processing system to said first data processing system.

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2. (Original) The method of claim 1 further including the step of advancing indicators in said first data processing system in preparation of transmitting another message, whereby a number of messages may be sent in rapid sequence.